



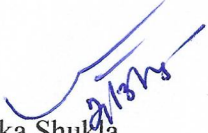
**RE-EXAMINATION TIMETABLE, March 2025**  
**BACHELOR OF TECHNOLOGY (B. Tech.) All Branches**  
**FIRST YEAR, SEM – I (for 2022-23 to 2024-25 Batches Under Autonomy)**

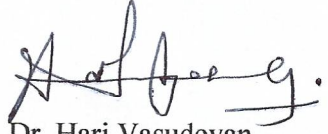
Date: 21<sup>st</sup> March, 2025

**Course Name: Engineering Graphics**

Day & Date	DIV / Batch	TIME	Venue
Monday 1 <sup>st</sup> April, 2025	All Branches	9.00 AM to 11.00 PM	Language Lab (2 <sup>nd</sup> Floor)

**Important Note: Changes if any, in the timetable, shall be communicated on the College website (djsce.ac.in).**

  
Ms. Alka Shukla  
Controller of Examinations

  
Dr. Hari Vasudevan  
Principal